GOA UNIVERSITY

ENGINEERING EXAMINATION SYSTEM

COLLEGE WISE STUDENT RESULT REGISTER FOR SEMESTER-VII EXAM

Page : 1 Of 9

RC 2016-17

Bachelor of Engineering (Electrical & Electronics) College :-Government of Goa College of Engineering

EXAM YEAR:November 2019

SEAT NO	P.R. NO.	ATTEMPTS	ARE CD	PRO SEM ESTE R-VII		NAME OF THE CANDIDATE	MAX . MIN .	THEORY 100 40	SESSIONAL 25	TERM WORK 25	TOTAL 125 OR 150 50 OR 60	PRACTICAL 50 OR 25 25 OR 10		TOTAL 725 290	REMARKS
190534	201610514	1	A		М	ABDAR AKS	HAY SH	IRIPATI							
	Switchgear	and Protect	ion					72	16		88P				
	Advanced D							54	18		72P				
	VLSI Circuit							39\$1	17		56P \$1	22P			
	Smart Grid	5						76	14		90P		21P		
	Fuzzy Logic	and Neural	Netwo	orks				66	19		85P		19P		
	Project												23P		
														476 \$1	Passes
190535	201610516	1	А		М	BAKHLE AM	OGH C		SHEKAR						
100000	Switchgear	-			101	B/ d d i LE / dvi	00110	58	14		72P				
	Advanced D							22	15		37F				
			ontroi					41	13		54P	19P			
	VLSI Circuit Smart Grid	Design						71	11		82P		19P		
	Fuzzy Logic	and Noural	Notw	orke				61	17		78P		18P		
	Project		INELWO	UIKS				01	17		701		22P		
	FIOJECI													401	Fails
														101	1 8115
190536	201611239	1	A		М	BANDEKAR	AMEY								
	Switchgear							44	13		57P				
	Advanced D		ontrol					42	15		57P				
	VLSI Circuit	Design						24	13		37F	19P			
	Smart Grid							63	11		74P		18P		
	Fuzzy Logic	and Neural	Netwo	orks				57	13		70P		19P		
	Project												23P		
														374 #7	Fails
190537	201610518	1	А		М	BHAT KAUSI	HIK AM	OL							
	Switchgear	and Protect	ion					41	17		58P				
	Advanced D	rives and C	ontrol					26	22		48F				
	VLSI Circuit	Design						59	14		73P	25P			
	Smart Grid							71	13		84P		22P		
	Fuzzy Logic	and Neural	Netwo	orks				54	20		74P		23P		
	Project												24P		
														431	Fails
190538	201610520	1	А		F	BHAT TERES	SA								
	Switchgear	and Protecti			-			61	20		81P				
	Advanced D							52	25		77P				
	VLSI Circuit		ontrol					86	23		109P	25P			
	Smart Grid	Design						80	21		101P		23P		
	Fuzzy Logic	and Neural	Netwo	orks				76	23		99P		24P		
	Project		110100	01110									24P		
	1 10,000													563	Passes
100500	201010500	4	٨		N 4										
190539	201610526	1	A		Μ	DHARGALKA	AR ANA			IKAN	075				
	Switchgear							69	18		87P				
	Advanced D		ontrol					30	20		50F	100			
	VLSI Circuit	Design						51	18		69P	18P	a : =		
	Smart Grid							79	14		93P		21P		
	Fuzzy Logic	and Neural	Netwo	orks				66	17		83P		22P		
	Project												23P		

190540	201610528 1 A M DHOND DATTAI							
	Switchgear and Protection	75	19	94P				
	Advanced Drives and Control	56	24	80P				
	VLSI Circuit Design	82	20	102P	25P			
	Smart Grid	83	20	103P		22P		
	Fuzzy Logic and Neural Networks	76	24	100P		24P		
	Project					23P		
							573	Passes

GOA UNI	IVERSITY		00111												Page : 2 Of 9
	Engineering (Elect				VISE	STUDENT RE	SULI	REGISTE	R FOR SEM	IESTER-VII E	=XAM				
	f Engineering (Elect overnment of Goa C			-											RC 2016-17
_		-	-						1	I	1				/ember 2019
SEAT NO	P.R. NO. A	ATTEMPTS	ARE CD	PRO SEM ESTE R-VII	SEX	NAME OF THE CANDIDATE	MAX. MIN.	THEORY 100 40	SESSIONAL 25	TERM WORK 25	TOTAL 125 OR 150 50 OR 60	PRACTICAL 50 OR 25 25 OR 10	ORAL 50 OR 25 25 OR 10	TOTAL 725 290	REMARK
190541	201610530	1	A		М	DIAS LENNA	RD								
	Switchgear and	d Protecti						68	23		91P				
	Advanced Driv							28	23		51F				
	VLSI Circuit De							72	22		94P	25P			
	Smart Grid							80	15		95P		22P		
	Fuzzy Logic ar	nd Neural	Netwo	orks				66	21		87P		23P		
	Project												22P		
	-													510	Fails
190542	201610531	1	А		М	FERNANDES	S OSC	AR AVEL	NO						
	Switchgear and	d Protecti	ion					60	12		72P				
	Advanced Driv	es and C	ontrol					40	15		55P				
	VLSI Circuit De	esign						30	14		44F	15P			
	Electrical Desi	ign Estima	ation a	nd Co	sting			54	11		65P		13P		
	Fuzzy Logic ar	nd Neural	Netwo	orks				70	13		83P		14P		
	Project												19P		
														380	Fails
190543	201610532	1	А		М	FERNANDES	S SHE\	WYN CYF	RUS						
	Switchgear and	d Protecti	ion					84	20		104P				
	Advanced Driv	es and C	ontrol					52	25		77P				
	VLSI Circuit De	esign						86	22		108P	25P			
	Smart Grid							82	20		102P		22P		
	Fuzzy Logic ar	nd Neural	Netwo	orks				60	24		84P		24P		
	Project												24P		
														570	Passes
190544	201610535	1	А		М	GAD NIKHIL	NILKA	NTH							
	Switchgear and	d Protecti	ion					68	15		83P				
	Advanced Driv	ves and C	ontrol					40	17		57P				
	VLSI Circuit De	esign						35#5	13		48P #5	14P			
	Electrical Desi	ign Estima	ation a	nd Co	sting			46	13		59P		13P		
	Fuzzy Logic ar	nd Neural	Netwo	orks				54	13		67P		20P		
	Project												20P		
														381 #7	Passes
190545	201610537	1	А		М	GADEKAR S	AHIL F	RAVINDR	٩						
	Switchgear and	d Protecti	ion					76	19		95P				
	Advanced Driv	ves and C	ontrol					28	19		47F				
	VLSI Circuit De	esign						47	17		64P	18P			
	Smart Grid							77	14		91P		20P		
	Fuzzy Logic ar	nd Neural	Netwo	orks				71	16		87P		23P		
	Project												23P		
														468	Fails
190546	201610540	1	А		М	GAONKAR A	SHOK	ANANT							
	Switchgear and	d Protecti	ion					55	15		70P				
	Advanced Driv	es and C	ontrol					44	21		65P				
	VLSI Circuit De	esign						49	18		67P	21P			
	Electrical Desi	-			sting			72	18		90P		19P		
	Fuzzy Logic ar	nd Neural	Netwo	orks				68	22		90P		22P		
	Project												23P		

	190547	201610542	1	A		М	GAONKAR OMKAR DINESH
--	--------	-----------	---	---	--	---	----------------------

Switchgear and Protection	52	18	70P				
Advanced Drives and Control	50	18	68P				
VLSI Circuit Design	49	17	66P	19P			
Electrical Design Estimation and Costing	64	19	83P		16P		
Fuzzy Logic and Neural Networks	68	19	87P		21P		
Project					22P		
						452 #7	Passes

GOA L	JNIVERSITY
-------	------------

COLLEGE WISE STUDENT RESULT REGISTER FOR SEMESTER-VII EXAM

Page : 3 Of 9

SEAT NO	P.R. NO.	ATTEMPTS	ARE	PRO	SEX	NAME OF THE				TERM WORK	TOTAL	PRACTICAL	ORAL	TOTAL	ember 2019
			CD	SEM ESTE R-VII		CANDIDATE	MAX . MIN .	100 40	25	25	125 OR 150 50 OR 60	50 OR 25 25 OR 10	50 OR 25 25 OR 10	725 290	REMARK
190548	201610556	1	А		М	GAONKAR P	RAJYC	T PRAB	HAKAR						
	Switchgear a	and Protect	ion					72	14		86P				
	Advanced D							40	17		57P		_		
	VLSI Circuit	Desian						59	13		72P	20P			
	Smart Grid	5						75	10		85P		19P		
	Fuzzy Logic	and Neural	Netwo	orks				68	19		87P		22P		
	Project												23P		
	,													471 #10	Passes
190549	201610562	1	А		М	GAUNEKER	VISHW	AMBHAF	RAMDAS						
	Switchgear a	and Protect	ion					61	11		72P				
	Advanced D							48	24		72P				
	VLSI Circuit	Design						42	19		61P	20P			
	Electrical De		ation a	nd Co	sting			73	20		93P		18P		
	Fuzzy Logic	-			5			60	21		81P		20P		
	Project												22P		
														459	Passes
190550	201705111	1	А		М	KAMAT NEH	AL RAN	/CHAND	RA						
	Switchgear a	and Protect	ion					68	14		82P				
	Advanced D							46	20		66P				
	VLSI Circuit							50	12		62P	19P			
	Electrical De	-	ation a	nd Co	stina			66	17		83P		16P		
	Fuzzy Logic	-			sung			60	14		74P		19P		
	Project			01110									24P		
	1.10,000													445	Passes
190551	201611233	1	А		М	KANTAK SAL	JRAV S								
	Switchgear a	and Protect	ion					64	16		80P				
	Advanced D							40	13		53P		_		
	VLSI Circuit		ontroi					36#4	15		51P #4	22P			
	Electrical De	-	ation a	nd Co	stina			52	15		67P		16P		
	Fuzzy Logic	-			sung			64	15		79P		19P		
	Project		NCLW	01113									23P		_
														410 #7	Passes
190552	201705139	1	А		F	KARMALKAR	SHAR	VANI SA	NJAY						
	Switchgear a	and Protect	ion					60	11		71P				
	Advanced D							42	18		60P				
	VLSI Circuit							37\$3	14		51P \$3	14P			
	Electrical De	-	ation a	nd Co	stina			49	17		66P		15P		
	Fuzzy Logic	-			sung			48	18		66P		19P		
	Project			onto									21P		
														383 \$3	Passes
190553	201610937	1	A		F	KERKAR NE		RAYAN							
	Switchgear a	and Protect		1				44	15		59P				
	Advanced D							26	15		41F				
	VLSI Circuit		Unuol					18	13		31F	15P			
	Electrical De	-	ation o	nd Co	sting			61	13		73P		13P		
	LICUIUAI DE	Juli Louina	auond		sung						-		-		
	Fuzzy Logic	and Noural	Notw	orke				50	16		66P		12P		

	190554	201610570	1	А		М	KERKAR RAHUL DAMODAR	
--	--------	-----------	---	---	--	---	----------------------	--

Switchgear and Protection	60	12	72P				
Advanced Drives and Control	44	21	65P				
VLSI Circuit Design	56	15	71P	24P			
Electrical Design Estimation and Costing	58	13	71P		15P		
Fuzzy Logic and Neural Networks	66	22	88P		20P		
Project					22P		
						448	Passes

GOA UNIVERSITY	
----------------	--

Page : 4 Of 9

	P.R. NO.	ATTEMPTS	ARE CD	PRO SEM ESTE R-VII	SEX	NAME OF THE CANDIDATE	MAX . MIN .	THEORY 100 40	SESSIONAL 25	TERM WORK 25	TOTAL 125 OR 150 50 OR 60	PRACTICAL 50 OR 25 25 OR 10	ORAL 50 OR 25 25 OR 10	TOTAL 725	REMARK
190555	201610571	1	A	K-VII	М	KESARKAR	SAI SA	NDESH							
	Switchgear a	and Protecti	on					70	13		83P				
	Advanced D	rives and C	ontrol					18	18		36F				
	VLSI Circuit	-						53	13		66P	21P			
	Electrical De	-			sting			73 55	14		87P 74P		18P 20P		
	Fuzzy Logic Project	and Neural	Netwo	orks				55	19		748		20P 23P		
	Tibject												-0.	428	Fails
190556	201704892	1	A		М	KHANDOLK	R TE.I	ΝΑΤΗ ΠΤ	ТАМ						
100000	Switchgear a	-						74	11		85P				
	Advanced D							26	16		42F				
	VLSI Circuit	Design						39#1	10		49P #1	14P			
	Electrical De	sign Estima	ation a	nd Co	sting			61	15		76P		19P		
	Fuzzy Logic	and Neural	Netwo	orks				35#5	15		50P #5		18P		
	Project												20P		
														373 #7	Fails
190557	201610576	1	A		М	KHOBREKA	R RAHL			1					
	Switchgear a							64	12		76P				
	Advanced D		ontrol					42 50	19		61P 66P	24P			
	VLSI Circuit Electrical De	-	ation	nd Ca	sting			50 63	16		78P	27F	22P		
	Fuzzy Logic	-			sung			64	17		81P		23P		
	Project		1101110	51110									23P		
	-								1					454	Passes
190558	201610579	1	А		F	KOLAMBKA	R PRAJ	AKTA PR	AKASH		RESULT WI	THHELD-(SEM.5	5 NOT PASSI	ED)	
	Switchgear a	and Protecti	on					70	15		85P				
	Advanced D							40	12		52P				
	VLSI Circuit	Design						29	15		44F	15P			
	Electrical De	sign Estima	ation a	nd Co	sting			50	13		63P		16P		
	Fuzzy Logic	and Neural	Netwo	orks				51	12		63P		15P		
	Project				_								21P		
190559	201704929	1	A		F	KOTTALGI V	IBHA	10	00	1	000				
	Switchgear a Advanced D							46 42	20 22		66P 64P				
	VLSI Circuit		ontroi					63	21		84P	21P			
	Electrical De	_	ation a	nd Co	stina			76	25		101P		20P		
	Fuzzy Logic	-			5g			61	20		81P		21P		
	Project												22P		
	Fillect													480	Passes
	Floject				М	KSHITIJ AJA	Y DESI	HMUKH							
190560	201610749	1	А							1	77P				
190560		-				1		62	15		//P				
190560	201610749 Switchgear a Advanced D	and Protecti rives and C	on					46	15		61P				
190560	201610749 Switchgear a Advanced D VLSI Circuit	and Protecti rives and C Design	on ontrol					46 55	15 18		61P 73P	20P			
190560	201610749 Switchgear a Advanced D VLSI Circuit Electrical De	and Protecti rives and C Design sign Estima	on ontrol ation a					46 55 60	15 18 16		61P 73P 76P	20P	19P		
190560	201610749 Switchgear a Advanced D VLSI Circuit Electrical De Fuzzy Logic	and Protecti rives and C Design sign Estima	on ontrol ation a					46 55	15 18		61P 73P	20P	20P		
190560	201610749 Switchgear a Advanced D VLSI Circuit Electrical De	and Protecti rives and C Design sign Estima	on ontrol ation a					46 55 60	15 18 16		61P 73P 76P	20P	-	456 #7	Passes
	201610749 Switchgear a Advanced D VLSI Circuit Electrical De Fuzzy Logic Project	and Protecti rives and C Design sign Estima and Neural	on ontrol ation a Netwo		sting			46 55 60 67	15 18 16		61P 73P 76P	20P	20P	456 #7	Passes
190560	201610749 Switchgear a Advanced D VLSI Circuit Electrical De Fuzzy Logic Project 201610770	and Protecti rives and C Design sign Estima and Neural	on ontrol ation a Netwo			LALAN KUM	AR SIN	46 55 60 67	15 18 16		61P 73P 76P	20P	20P	456 #7	Passes
	201610749 Switchgear a Advanced D VLSI Circuit Electrical De Fuzzy Logic Project	and Protecti rives and C Design sign Estima and Neural 1 and Protecti	on ontrol ation a Netwo A on		sting		AR SIN	46 55 60 67 GH M.S.	15 18 16 20		61P 73P 76P 87P	20P	20P	456 #7	Passes
	201610749 Switchgear a Advanced D VLSI Circuit Electrical De Fuzzy Logic Project 201610770 Switchgear a	and Protecti rives and C Design sign Estima and Neural 1 and Protecti rives and C	on ontrol ation a Netwo A on		sting		AR SIN	46 55 60 67 GH M.S. 71	15 18 16 20 14		61P 73P 76P 87P	20P	20P	456 #7	Passes
	201610749 Switchgear a Advanced D VLSI Circuit Electrical De Fuzzy Logic Project 201610770 Switchgear a Advanced D	and Protecti rives and C Design sign Estima and Neural 1 and Protecti rives and C	on ontrol ation a Netwo A on		sting		AR SIN	46 55 60 67 GH M.S. 71 40	15 18 16 20 14 15		61P 73P 76P 87P 85P 55P		20P	456 #7	Passes
	201610749 Switchgear a Advanced D VLSI Circuit Electrical De Fuzzy Logic Project 201610770 Switchgear a Advanced D VLSI Circuit Smart Grid Fuzzy Logic	and Protecti rives and C Design sign Estima and Neural 1 and Protecti rives and C Design	on ontrol ation a Netwo A on ontrol	orks	sting		AR SIN	46 55 60 67 GH M.S. 71 40 57	15 18 16 20 14 15 15		61P 73P 76P 87P 87P 85P 55P 72P		20P 23P 19P 21P	456 #7	Passes
	201610749 Switchgear a Advanced D VLSI Circuit Electrical De Fuzzy Logic Project 201610770 Switchgear a Advanced D VLSI Circuit Smart Grid	and Protecti rives and C Design sign Estima and Neural 1 and Protecti rives and C Design	on ontrol ation a Netwo A on ontrol	orks	sting		AR SIN	46 55 60 67 GH M.S. 71 40 57 68	15 18 16 20 14 15 15 11		61P 73P 76P 87P 87P 85P 55P 72P 79P		20P 23P	456 #7	Passes

GOA UNIVERSITY

COLLEGE WISE STUDENT RESULT REGISTER FOR SEMESTER-VII EXAM

Page : 5 Of 9

SEAT NO	P.R. NO.	ATTEMPTS		PRO	SEX	NAME OF THE		THEORY	SESSIONAL	TERM WORK	TOTAL	PRACTICAL		TOTAL	ember 2019
			CD	SEM ESTE R-VII		CANDIDATE	MAX . MIN .	100 40	25	25	125 OR 150 50 OR 60	50 OR 25 25 OR 10		725 290	REMARK
190562	201610464	1	А		М	MALWANKEI	R SAH	IL MAHA	RUDRA						
	Switchgear	and Protect	ion					68	16		84P				
	Advanced D	rives and C	ontrol					52	24		76P				
	VLSI Circuit	Design						47	15		62P	24P			
	Smart Grid							67	15		82P		22P		
	Fuzzy Logic	and Neural	Netw	orks				70	20		90P		22P		
	Project												22P		
														484	Passes
190563	201611242	1	А		М	MARGAJ SH	UBHAN	/ PRAKA	SH						
	Switchgear	and Protect	ion			1		73	13		86P				
	Advanced D							30	7		37F				
	VLSI Circuit	Design						45	10		55P	14P			
	Electrical De		ation a	ind Co	sting			62	11		73P		15P		
	Fuzzy Logic and Neural Networks							50	12		62P		14P		
	Project			-								-	16P		-
	,													372	Fails
190564	201610595	1	Α		М	NAIK PRABH	IAV RA	JENDRA							
	Switchgear							42	15		57P				_
	Advanced D							28	15		43F				
	VLSI Circuit		ontioi					50	19		69P	20P			
	Smart Grid	Design						76	11		87P	201	20P		
	Fuzzy Logic	and Neural	Notw	orke				58	17		75P		19P		
	Project		INCLW	UIKS									23P		_
	110,000													413	Fails
190565	201610598	1	А		М	NAIK PRAJY									
190303		-			IVI			67	15		82P				_
	Switchgear a Advanced D							40	20		60P				
			ontroi					58	18		76P	20P			
	VLSI Circuit	•	otion o		otina			62	22		84P	206	20P		
	Electrical De	-			sung			67	15		82P		19P		
	Fuzzy Logic Project	and Neural	netw	UIKS				07	15		021		23P		
	Floject												201	466 #7	Passes
400500	004040500		•		F									400 ///	1 23553
190566	201610599	1	A		F	NAIK PRIYAN	NKA PA								
	Switchgear							40	12		52P				
	Advanced D		ontrol					24	12		36F	105			
	VLSI Circuit	_						45	15		60P	19P	10-		
	Electrical De	-			sting			42	8		50P		13P		
	Fuzzy Logic	and Neural	Netw	orks				40	18		58P		16P		
	Project												22P	000	
						1								326	Fails
190567	201705112	1	A		М	NAIK SAEEL	PRAK								
	Switchgear							66	16		82P				
	Advanced D	rives and C	ontrol					40	13		53P				
	VLSI Circuit	Design						35#5	15		50P #5	18P			
	Electrical De				sting			68	23		91P		20P		
	Fuzzy Logic	and Neural	Netw	orks				61	13		74P		16P		
	Project							1		1			22P		

Switchgear and Protection	52	11	63P				
Advanced Drives and Control	28	15	43F				
VLSI Circuit Design	49	15	64P	23P			
Smart Grid	76	10	86P		20P		
Fuzzy Logic and Neural Networks	56	17	73P		21P		
Project					24P		
						417	Fails

GOA UNIVERSITY	
----------------	--

Page : 6 Of 9

P.R. NO. 201610605 Switchgear a Advanced D VLSI Circuit Electrical De Fuzzy Logic Project 201610607	rives and C Design sign Estima	ARE CD A	PRO SEM ESTE	SEX	NAME OF THE			OFCOMAL	TERM WORK			ORAL	TOTAL	
Switchgear a Advanced D VLSI Circuit Electrical De Fuzzy Logic Project 201610607	and Protecti rives and C Design sign Estima	А	R-VII		CANDIDATE	MAX . MIN .	THEORY 100 40	SESSIONAL 25	TERM WORK 25	TOTAL 125 OR 150 50 OR 60	PRACTICAL 50 OR 25 25 OR 10	50 OR 25 25 OR 10		REMARK
Advanced D VLSI Circuit Electrical De Fuzzy Logic Project 201610607	rives and C Design sign Estima			М	NAIK SAISH	KISHO								
VLSI Circuit Electrical De Fuzzy Logic Project 201610607	Design sign Estima						66	17		83P				
Electrical De Fuzzy Logic Project 201610607	sign Estima	ontrol					40 35\$5	5 11		45P \$5 46P \$5	14P			
Fuzzy Logic Project 201610607	-	ation a	nd Cos	stina			50	11		61P	141	18P		
Project 201610607				Jung			48	15		63P		15P		
												15P		
													360 \$10	Passes
	1	A		F	NAIK SONIA	ANANE)							
Switchgear a	and Protecti	on					70	14		84P				
Advanced D		ontrol					42	15		57P				
VLSI Circuit	-						44	18		62P	18P	100		
Electrical De	-			sting			57	15		72P		16P		
	and Neural	Netwo	orks				04	20		0417				
FIUJECI													427	Passes
201610609	1	Δ		F	ΝΔΙΚ ΤΕ ΙΔΟ			<u>م</u>						
		·			58	12		70P						
Advanced Drives and Control								20		60P		_		
							46	16		62P	24P			
Electrical De	sign Estima	ation a	nd Cos	sting			55	17		72P		16P		
	and Neural	Netwo	orks				60	18		78P		19P		
Project												23P		
													424	Passes
201610611	1	A		М	NARAYANPU	JR WAS		AM ABDUL						
-								16						
		ontrol									450			
	-	tion of		tina				-			15P	14P		
	-			sung										
		Netwo					-					20P		
,													345	Fails
201611231														
	1			М	PARAB AJIN	KYA BA	ALA 🛛			RESULT WI	THHELD-(SEM.	5 NOT PASSI	ED)	
Switchgear a				Μ	PARAB AJIN	KYA BA	ALA 64	12		RESULT WI	THHELD-(SEM.	5 NOT PASSI	ED)	
	and Protecti	on		Μ	PARAB AJIN	KYA BA		12 17			THHELD-(SEM.	5 NOT PASSI	ED)	
Switchgear a Advanced D VLSI Circuit	and Protecti rives and C Design	on ontrol			PARAB AJIN	KYA BA	64 14 35\$5	17 14		76P 31F 49P \$5	THHELD-(SEM. 18P		ED)	
Switchgear aAdvanced DVLSI CircuitElectrical De	and Protecti rives and C Design sign Estima	on ontrol ation ai			PARAB AJIN	KYA BA	64 14 35\$5 41	17 14 10		76P 31F 49P \$5 51P		14P	ED)	
Switchgear a Advanced D VLSI Circuit Electrical De Fuzzy Logic	and Protecti rives and C Design sign Estima	on ontrol ation ai			PARAB AJIN	KYA BA	64 14 35\$5	17 14		76P 31F 49P \$5		14P 16P	ED)	
Switchgear a Advanced D VLSI Circuit Electrical De Fuzzy Logic Project	and Protecti rives and C Design sign Estima and Neural	on ontrol ation ar Netwo		sting			64 14 35\$5 41 60	17 14 10 13		76P 31F 49P \$5 51P		14P	ED)	
Switchgear aAdvanced DVLSI CircuitElectrical DeFuzzy LogicProject201610614	and Protecti rives and C Design sign Estima and Neural	on ontrol ation ar Netwo			PARAB AJIN PATIL SAUR		64 14 35\$5 41 60 ARASHA	17 14 10 13 RAM		76P 31F 49P \$5 51P 73P		14P 16P	ED)	
Switchgear aAdvanced DVLSI CircuitElectrical DeFuzzy LogicProject201610614Switchgear a	and Protecti rives and C Design sign Estima and Neural 1 and Protecti	on ontrol ation ar Netwo A on		sting			64 14 35\$5 41 60 ARASHA	17 14 10 13 RAM 19		76P 31F 49P \$5 51P 73P 85P		14P 16P	ED)	
Switchgear a Advanced D VLSI Circuit Electrical De Fuzzy Logic Project 201610614 Switchgear a Advanced D	and Protecti rives and C Design sign Estima and Neural 1 and Protecti rives and C	on ontrol ation ar Netwo A on		sting			64 14 35\$5 41 60 ARASHA	17 14 10 13 RAM		76P 31F 49P \$5 51P 73P		14P 16P	ED)	
Switchgear aAdvanced DVLSI CircuitElectrical DeFuzzy LogicProject201610614Switchgear a	and Protecti rives and C Design sign Estima and Neural 1 and Protecti rives and C	on ontrol ation ar Netwo A on		sting			64 14 35\$5 41 60 ARASHA 66 42	17 14 10 13 RAM 19 24		76P 31F 49P \$5 51P 73P 85P 66P	18P	14P 16P	ED)	
Switchgear a Advanced D VLSI Circuit Electrical De Fuzzy Logic Project 201610614 Switchgear a Advanced D VLSI Circuit Smart Grid	and Protecti rives and C Design sign Estima and Neural 1 and Protecti rives and C Design	on ontrol ation ar Netwo A on ontrol	rks	sting			64 14 35\$5 41 60 ARASHA 66 42 77	17 14 10 13 RAM 19 24 22		76P 31F 49P \$5 51P 73P 85P 66P 99P	18P	14P 16P 23P	ED)	
Switchgear a Advanced D VLSI Circuit Electrical De Fuzzy Logic Project 201610614 Switchgear a Advanced D VLSI Circuit	and Protecti rives and C Design sign Estima and Neural 1 and Protecti rives and C Design	on ontrol ation ar Netwo A on ontrol	rks	sting			64 14 35\$5 41 60 ARASHA 66 42 77 74	17 14 10 13 RAM 19 24 22 16		76P 31F 49P \$5 51P 73P 85P 66P 99P 90P	18P	14P 16P 23P	ED)	
Switchgear a Advanced D VLSI Circuit Electrical De Fuzzy Logic Project 201610614 Switchgear a Advanced D VLSI Circuit Smart Grid Fuzzy Logic	and Protecti rives and C Design sign Estima and Neural 1 and Protecti rives and C Design	on ontrol ation ar Netwo A on ontrol	rks	sting			64 14 35\$5 41 60 ARASHA 66 42 77 74	17 14 10 13 RAM 19 24 22 16		76P 31F 49P \$5 51P 73P 85P 66P 99P 90P	18P	14P 16P 23P 23P	ED)	Passes
Switchgear a Advanced D VLSI Circuit Electrical De Fuzzy Logic Project 201610614 Switchgear a Advanced D VLSI Circuit Smitchgear a Advanced D VLSI Circuit Smart Grid Fuzzy Logic	and Protecti rives and C Design sign Estima and Neural 1 and Protecti rives and C Design	on ontrol ation ar Netwo A on ontrol	rks	sting		ABH PA	64 14 35\$5 41 60 ARASHA 66 42 77 74 61	17 14 10 13 RAM 19 24 22 16		76P 31F 49P \$5 51P 73P 85P 66P 99P 90P	18P	14P 16P 23P 23P		Passes
Switchgear aAdvanced DVLSI CircuitElectrical DeFuzzy LogicProject201610614Switchgear aAdvanced DVLSI CircuitSmart GridFuzzy LogicProject	and Protecti rives and C Design sign Estima and Neural 1 and Protecti rives and C Design and Neural 1 and Protecti	on ontrol ation ar Netwo A on Netwo A on	rks	M	PATIL SAUR	ABH PA	64 14 35\$5 41 60 ARASHA 66 42 77 74 61 61	17 14 10 13 RAM 19 24 22 16 21		76P 31F 49P \$5 51P 73P 85P 66P 99P 90P 82P 72P	18P	14P 16P 23P 23P		Passes
Switchgear a Advanced D VLSI Circuit Electrical De Fuzzy Logic Project 201610614 Switchgear a Advanced D VLSI Circuit Smart Grid Fuzzy Logic Project 201610767 Switchgear a Advanced D	and Protecti rives and C Design sign Estima and Neural 1 and Protecti rives and C Design and Neural 1 and Protecti rives and C	on ontrol ation ar Netwo A on Netwo A on	rks	M	PATIL SAUR	ABH PA	64 14 35\$5 41 60 ARASHA 66 42 77 74 61 41 61 26	17 14 10 13 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7		76P 31F 49P \$5 51P 73P 85P 66P 99P 90P 82P 72P 45F	18P 25P	14P 16P 23P 23P		Passes
Switchgear a Advanced D VLSI Circuit Electrical De Fuzzy Logic Project 201610614 Switchgear a Advanced D VLSI Circuit Smart Grid Fuzzy Logic Project 201610767 Switchgear a Advanced D VLSI Circuit	and Protecti rives and C Design sign Estima and Neural 1 and Protecti rives and C Design and Neural 1 and Protecti rives and C Design	on ontrol ation an Netwo A on Netwo A on ontrol	orks	M F	PATIL SAUR	ABH PA	64 14 35\$5 41 60 42 77 74 61 61 61 26 62	17 14 10 13 RAM 19 24 22 16 21 11 19 18		76P 31F 49P \$5 51P 73P 85P 66P 99P 90P 82P 72P 45F 80P	18P	14P 16P 23P 23P 21P 24P 24P		Passes
Switchgear a Advanced D VLSI Circuit Electrical De Fuzzy Logic Project 201610614 Switchgear a Advanced D VLSI Circuit Smart Grid Fuzzy Logic Project 201610767 Switchgear a Advanced D VLSI Circuit Electrical De	and Protecti rives and C Design sign Estima and Neural 1 and Protecti rives and C Design and Neural 1 and Protecti rives and C Design sign Estima	on ontrol ation an Netwo A on Netwo A on ontrol	orks orks	M F	PATIL SAUR	ABH PA	64 14 35\$5 41 60 ARASHA 66 42 77 74 61 26 62 63	17 14 10 13 RAM 19 24 22 16 21 11 19 18 19		76P 31F 49P \$5 51P 73P 85P 66P 99P 82P 72P 45F 80P 82P	18P 25P	14P 16P 23P 23P 21P 24P 24P 24P		Passes
Switchgear a Advanced D VLSI Circuit Electrical De Fuzzy Logic Project 201610614 Switchgear a Advanced D VLSI Circuit Smart Grid Fuzzy Logic Project 201610767 Switchgear a Advanced D VLSI Circuit	and Protecti rives and C Design sign Estima and Neural 1 and Protecti rives and C Design and Neural 1 and Protecti rives and C Design sign Estima	on ontrol ation an Netwo A on Netwo A on ontrol	orks orks	M F	PATIL SAUR	ABH PA	64 14 35\$5 41 60 42 77 74 61 61 61 26 62	17 14 10 13 RAM 19 24 22 16 21 11 19 18		76P 31F 49P \$5 51P 73P 85P 66P 99P 90P 82P 72P 45F 80P	18P 25P	14P 16P 23P 23P 21P 24P 24P		Passes
	Project 201610609 Switchgear a Advanced Di VLSI Circuit Electrical De Fuzzy Logic Project 201610611 Switchgear a Advanced Di VLSI Circuit Electrical De	Project 201610609 1 Switchgear and Protecti Advanced Drives and C VLSI Circuit Design Electrical Design Estimation Fuzzy Logic and Neural Project 201610611 Switchgear and Protecti Advanced Drives and C VLSI Circuit Design Electrical Design Estimation Fuzzy Logic and Neural Project	Project 201610609 1 A Switchgear and Protection Advanced Drives and Control Advanced Drives and Control VLSI Circuit Design Electrical Design Estimation ar Fuzzy Logic and Neural Network Project 201610611 1 Advanced Drives and Control VLSI Circuit Design Switchgear and Protection Advanced Drives and Control VLSI Circuit Design Electrical Design Estimation ar Fuzzy Logic and Neural Network Project	201610609 1 A Switchgear and Protection Advanced Drives and Control VLSI Circuit Design Electrical Design Estimation and Cost Fuzzy Logic and Neural Networks Project 201610611 1 Advanced Drives and Control VLSI Circuit Design Advanced Drives and Control VLSI Circuit Design Electrical Design Estimation and Cost Fuzzy Logic and Protection Advanced Drives and Control VLSI Circuit Design Electrical Design Estimation and Cost Fuzzy Logic and Neural Networks Project	Project 201610609 1 A F Switchgear and Protection Advanced Drives and Control V Advanced Drives and Control VLSI Circuit Design Electrical Design Estimation and Costing Fuzzy Logic and Neural Networks Project 201610611 1 A M Switchgear and Protection Advanced Drives and Control VLSI Circuit Design Electrical Design Estimation and Costing Fuzzy Logic and Neural Networks Fuzzy Logic and Neural Networks Fuzzy Logic and Neural Networks Project VLSI Circuit Design VLSI Circuit Design Fuzzy Logic and Neural Networks Project VISI Circuit Design VISI Circuit Design Fuzzy Logic and Neural Networks Project VISI Networks VISI Networks	Project 201610609 1 A F NAIK TEJAS Switchgear and Protection Advanced Drives and Control VLSI Circuit Design Electrical Design Estimation and Costing Fuzzy Logic and Neural Networks Project 201610611 1 A M NARAYANPL Switchgear and Protection Advanced Drives and Control VLSI Circuit Design Switchgear and Protection Advanced Drives and Control VLSI Circuit Design Electrical Design Estimation and Costing Fuzzy Logic and Neural Networks Project	Project 201610609 1 A F NAIK TEJASWINI TU Switchgear and Protection Advanced Drives and Control VLSI Circuit Design Electrical Design Estimation and Costing Fuzzy Logic and Neural Networks Project 201610611 1 A M NARAYANPUR WAS Switchgear and Protection Advanced Drives and Control VLSI Circuit Design 201610611 1 A M NARAYANPUR WAS Switchgear and Protection Advanced Drives and Control VLSI Circuit Design Electrical Design Estimation and Costing Electrical Design Estimation and Costing Fuzzy Logic and Neural Networks Project Project	Project 201610609 1 A F NAIK TEJASWINI TULSHIDA Switchgear and Protection 58 Advanced Drives and Control 40 VLSI Circuit Design 46 Electrical Design Estimation and Costing 55 Fuzzy Logic and Neural Networks 60 Project 60 Project 46 201610611 1 A M NARAYANPUR WASIM AKR/ Switchgear and Protection 46 46 46 Advanced Drives and Control 41 46 VLSI Circuit Design 29 29 29 Electrical Design Estimation and Costing 45 52 Project 52 9 52	Project 201610609 1 A F NAIK TEJASWINI TULSHIDAS Switchgear and Protection 58 12 Advanced Drives and Control 40 20 VLSI Circuit Design 46 16 Electrical Design Estimation and Costing 55 17 Fuzzy Logic and Neural Networks 60 18 Project 46 16 Switchgear and Protection 46 16 Switchgear and Neural Networks 60 18 Project 46 16 Switchgear and Protection 46 16 Advanced Drives and Control 46 16 Advanced Drives and Control 41 15 VLSI Circuit Design 29 6 Electrical Design Estimation and Costing 45 14 Fuzzy Logic and Neural Networks 52 15 Project 15 15	Project 201610609 1 A F NAIK TEJASWINI TULSHIDAS 201610609 1 A F NAIK TEJASWINI TULSHIDAS Switchgear and Protection 58 12 Advanced Drives and Control 40 20 VLSI Circuit Design 46 16 Electrical Design Estimation and Costing 55 17 Fuzzy Logic and Neural Networks 60 18 Project 60 18 201610611 1 A M NARAYANPUR WASIM AKRAM ABDUL Switchgear and Protection 46 Advanced Drives and Control 46 16 Switchgear and Protection 46 16 Switchgear and Protection 46 16 Advanced Drives and Control 41 15 VLSI Circuit Design 29 6 Electrical Design Estimation and Costing 45 14 Fuzzy Logic and Neural Networks 52 15 Project i i i	Project 201610609 1 A F NAIK TEJASWINI TULSHIDAS 201610609 1 A F NAIK TEJASWINI TULSHIDAS 70P Advanced Drives and Control 40 20 60P VLSI Circuit Design 46 16 62P Electrical Design Estimation and Costing 55 17 72P Fuzzy Logic and Neural Networks 60 18 78P Project 201610611 1 A M NARAYANPUR WASIM AKRAM ABDUL 1 Switchgear and Protection 46 16 62P 62P 62P 62P Switchgear and Protection 46 16 62P 62P	Project Image: Constraint of the image: Constraint	Project Image: Section From the interval of the	Project Image <

GOA UNI	VERSITY
---------	---------

COLLEGE WISE STUDENT RESULT REGISTER FOR SEMESTER-VII EXAM

Page : 7 Of 9

SEAT NO	P.R. NO.	ATTEMPTS	ARE CD	PRO SEM ESTE	SEX	NAME OF THE CANDIDATE	MAX . MIN .	THEORY 100 40	SESSIONAL 25	TERM WORK 25	TOTAL 125 OR 150 50 OR 60	PRACTICAL 50 OR 25 25 OR 10	ORAL 50 OR 25 25 OR 10	TOTAL 725 290	ember 2019 REMARKS
400577	004040040	4	^	R-VII	-										
190577	201610618	1	A		F	SAKROJI VE	DA VIN								
	Switchgear a							64	11		75P				
	Advanced D		ontrol					40	17		57P				
	VLSI Circuit	-						45	17		62P	17P			
	Electrical De	-			sting			56	13		69P		12P		
	Fuzzy Logic	and Neura	Netw	orks				52	10		62P		15P		
	Project												20P		
														389 #7	Passes
190578	201610622	1	А		М	SALUNKHE	SIDDHA	ANTA SH	RIKANT						
	Switchgear a	and Protect	ion					53	13		66P				
	Advanced D	rives and C	ontrol					26	22		48F				
	VLSI Circuit	Design						52	19		71P	20P			
	Electrical De	sign Estima	ation a	nd Co	sting			51	13		64P		14P		
	Fuzzy Logic	and Neura	Netw	orks	-			40	19		59P		18P		
	Project												22P		
														382 #7	Fails
190579	201610628	1	А		F	SANDILYA P	OOJA S	SIYARAM	1						
	Switchgear a	and Protect	ion					60	17		77P				
	Advanced D							40	22		62P				
	VLSI Circuit							57	23		80P	25P			
	Electrical De	0	ation a	nd Co	stina			64	21		85P		20P		
	Fuzzy Logic							61	24		85P		24P		
	Project												23P		
														481	Passes
190580	201704889	1	А		М	SANGEKAR			ODAR						
100000	Switchgear a				101	O/ ITOEIT/ IT		69	16		85P				
	Advanced D							44	22		66P				
	VLSI Circuit		ontioi					48	15		63P	20P			
	Electrical De	-	ation a	nd Co	etina			56	16		72P		16P		
	Fuzzy Logic	-			Sung			55	16		71P		18P		
	Project		INCLW	UIKS									23P		
	1 10j001													434 #7	Passes
190581	201610632	1	А		М	SANGUEKA									
190301					IVI	SANGUERAN	N FNAC	70	15		85P				
	Switchgear a							50	22		72P				
	Advanced D		ontrol									00D			
	VLSI Circuit	-	-4.		- 4! -			75 72	20 20		95P 92P	22P	19P		
	Electrical De	-			sting						92P 94P		19P		
	Fuzzy Logic	and Neura	Netw	orks				75	19		94P		-		
	Project												22P	517 #7	Doocoo
														JT #1	Passes
190582	201711962	1	A		М	SAWANT KA	USHAL								
	Switchgear a							68	19		87P				
	Advanced D		ontrol					40	22		62P				
	VLSI Circuit	-						72	20		92P	20P			
	Electrical De	-			sting			74	23		97P		20P		
				I				77	18	1	95P	1	19P	1	
	Fuzzy Logic Project	and Neura	Netw	orks				11	10		501		23P		

Switchgear and Protection	67	13	80P				
Advanced Drives and Control	40	21	61P				
VLSI Circuit Design	39#1	12	51P #1	20P			
Electrical Design Estimation and Costing	61	17	78P		16P		
Fuzzy Logic and Neural Networks	48	13	61P		18P		
Project					22P		
						407 #7	Passes

GUA UNIVERSITI	GOA	UNIVERSITY	/
----------------	-----	------------	---

COLLEGE WISE STUDENT RESULT REGISTER FOR SEMESTER-VII EXAM

Page : 8 Of 9

	P.R. NO.	ATTEMPTS		SEM ESTE	SEX	NAME OF THE CANDIDATE	MAX. MIN.	THEORY 100 40	SESSIONAL 25	TERM WORK 25	TOTAL 125 OR 150 50 OR 60	PRACTICAL 50 OR 25 25 OR 10	ORAL 50 OR 25 25 OR 10	TOTAL 725 290	REMARK
190584	201704891	1	A	R-VII	М	SHAIK MOH	AMMAE) SAFIR			RESULT WI	THHELD-(SEM.5	5 NOT PASSE	ED)	
	Switchgear a	and Protecti	on					40	12		52P			,	
	Advanced D							28	14		42F				
	VLSI Circuit	Design						25	14		39F	23P			
	Electrical De	sign Estima	ation ar	nd Cos	ting			47	12		59P		19P		
	Fuzzy Logic	and Neural	Netwo	orks				50	10		60P		18P		
	Project												24P		
190585	201704890	1	А		М	SHET PARSI	EKAR F	PRANAV	VITHAL PR	AKASH					
	Switchgear a	and Protecti	on					66	14		80P				
	Advanced D		ontrol					50	16		66P				
	VLSI Circuit							22	10		32F	12P			
	Electrical De	-			sting			58	12		70P		14P		
	Fuzzy Logic	and Neural	Netwo	orks				58	12		70P		17P		
	Project												22P	202	
														383	Fails
90586	201610642	1	A		М	SHETKER R	UTHVI								
	Switchgear							68	12		80P				
	Advanced D		ontrol					20	18		38F	150			
	VLSI Circuit	-	4!		41			47	12 11		59P	15P	150		
	Electrical De	-			ang			54 57	11		65P 68P		15P 14P		
	Fuzzy Logic Project	and neural	Netwo	orks				57	11		UOF		14F		
	FIOJECI												101	373	Fails
00507	004040744	4	•											010	1 4115
90587	201610744		A		М	SUHAIL FIRC	JZ KHA		10		04.0				
	Switchgear a							72 40	19 25		91P 65P				
	VLSI Circuit		Shiroi					80	17		97P	21P			
	Smart Grid	Design						82	20		102P	211	21P		
		and Neural	Netwo	orks				83	23		106P		24P		
	Fuzzy Logic and Neural Networks Project												22P		
	Project													549	Passes
190588	201610760	1	A		М	SWAGAT KU	IMAR M		Y						
100000	Switchgear a	and Protecti						57	16		73P				
	Advanced D							44	24		68P				
	VLSI Circuit							38\$2	19		57P \$2	20P			
	Smart Grid							79	12		91P		20P		
	Fuzzy Logic	and Neural	Netwo	orks				72	21		93P		23P		
	Project												23P		
														468 \$2	Passes
90589	201610762	1	А		F	SWASTIKA									
	Switchgear a	and Protecti	on					40	12		52P				
	Advanced D							40	18		58P				
		D						35\$5	18		53P \$5	21P			
	VLSI Circuit	Design						57	13		70P		16P		
	VLSI Circuit Electrical De	-	ation ar	nd Cos	sting			52	18		70P		23P		
		sign Estima			sting								22P		
	Electrical De	sign Estima			sting										
	Electrical De Fuzzy Logic	sign Estima			sting						1			385 \$5	Passes
190590	Electrical De Fuzzy Logic	sign Estima		orks	M	TALAULIKAF	R SAHA	RSH HUI	NUMANT					385 \$5	Passes
90590	Electrical De Fuzzy Logic Project	esign Estima and Neural	Netwo	orks		TALAULIKAF	R SAHA	RSH HUI	NUMANT 10		82P			385 \$5	Passes
190590	Electrical De Fuzzy Logic Project 201610645	and Neural	Netwo A on	orks		TALAULIKAF	R SAHA				82P 72P			385 \$5	Passes
190590	Electrical De Fuzzy Logic Project 201610645 Switchgear a Advanced D VLSI Circuit	and Neural and Neural 1 and Protecti rives and C Design	A on ontrol	orks	M	TALAULIKAF	R SAHA	72 52 51	10 20 12		72P 63P	14P		385 \$5	Passes
190590	Electrical De Fuzzy Logic Project 201610645 Switchgear a Advanced D VLSI Circuit Electrical De	and Neural and Neural 1 and Protecti rives and C Design esign Estima	A on ontrol	orks	M	TALAULIKAF	R SAHA	72 52 51 55	10 20 12 13		72P 63P 68P	14P	15P	385 \$5	Passes
190590	Electrical De Fuzzy Logic Project 201610645 Switchgear a Advanced D VLSI Circuit Electrical De Fuzzy Logic	and Neural and Neural 1 and Protecti rives and C Design esign Estima	A on ontrol	orks	M	TALAULIKAF	R SAHA	72 52 51	10 20 12		72P 63P	14P	15P	385 \$5	Passes
190590	Electrical De Fuzzy Logic Project 201610645 Switchgear a Advanced D VLSI Circuit Electrical De	and Neural and Neural 1 and Protecti rives and C Design esign Estima	A on ontrol	orks	M	TALAULIKAF	R SAHA	72 52 51 55	10 20 12 13		72P 63P 68P	14P	-	385 \$5	Passes

GOA UNIVER	SITY
-------------------	------

COLLEGE WISE STUDENT RESULT REGISTER FOR SEMESTER-VII EXAM

Page : 9 Of 9

SEAT NO	P.R. NO.	ATTEMPTS	ARE	PRO	SEX	NAME OF THE		THEORY	SESSIONAL	TERM WORK	TOTAL	PRACTICAL	ORAL	TOTAL	ember 2019
			CD	SEM ESTE R-VII		CANDIDATE	MAX . MIN .	100 40	25	25	125 OR 150 50 OR 60	50 OR 25 25 OR 10	50 OR 25 25 OR 10	725 290	REMARK
190591	201610647	1	A		F	TORASKAR	VAISHI		SHNA	1					
	Switchgear a	and Protecti	on					60	15		75P				
	Advanced D	rives and C	ontrol					40	21		61P				
	VLSI Circuit	Design						67	21		88P	18P			
	Smart Grid							68	14		82P		20P		
	Fuzzy Logic	and Neural	Netwo	orks				58	21		79P		20P		
	Project												23P		
														466	Passes
190592	201610649	1	А		М	VAZ KEWEL									
	Switchgear a	and Protecti	on					66	12		78P				
	Advanced D	rives and C	ontrol					40	22		62P				
	VLSI Circuit	Design						52	19		71P	20P			
	Smart Grid							73	12		85P		18P		
	Fuzzy Logic	and Neural	Netwo	orks				71	20		91P		23P		
	Project												24P		
														472 #7	Passes
190593	201610651	1	А		М	VERENKER	SHUBH	IAM ANIL							
	Switchgear a	and Protecti	on					58	15		73P				
	Advanced D	rives and C	ontrol					42	23		65P				
	VLSI Circuit	Design						60	18		78P	13P			
	Electrical De	esign Estima	ation a	nd Cos	sting			54	17		71P		19P		
	Fuzzy Logic and Neural Networks							56	16		72P		17P		
	Project												20P		

Read by

Checked by

Declared On

Assistant Registrar-E(Prof)

Assistant Registrar Exam

Controller of Examinations

P:Passes; F:Fails; Abs:Absent; N:Non Appearance; D:First Class with Distinction; I: First Class; S:Second Class; NE:Not Eligible; +:Marks carried over Discrepancy If any in the Result shall be informed to the University within a week from the date of receipt of the result by the colleges.